

FORM PTQ-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

ATTY. DOCKET NO.
30585/3SERIAL NO.
09/385,394

APPLICANT

John S. Yates, Jr. et al.

FILING DATE

August 30, 1999

GROUP PART UNIT

275

DEC 15 1999

RECEIVED

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	Group	FILING DATE IF APPROPRIATE
JC 82	3,544,969	12/01/70	Rakoczi	340	172.5		Nov. 27, 1967
DEC 13 1999 PATENT & TRADEMARK OFFICE	3,781,823	12/25/73	Senese	340	172.5		July 28, 1972
	4,084,235	04/11/78	Hirtle	364	200		Apr. 14, 1975
	4,455,602	06/19/84	Baxter	364	200		May 22, 981
	4,514,803	4/30/85	Agnew	364	200		Apr. 26, 1982
	4,575,797	03/11/86	Gruner	364	200		May 22, 1981
	5,574,927	11/12/96	Scantlin	712	041		Mar. 25, 1994
	5,638,525	06/10/97	Hammond	395	385		Feb. 10, 1995
	5,822,578	10/13/98	Frank	395	591		June 5, 1995
	5,832,205	11/03/98	Kelly	395	185.06		Aug. 20, 1996
	5,920,721	7/6/99	Hunter	395	705		Jun. 11, 1997
	5,930,509	07/27/99	Yates	395	707		Jan. 29, 1996
	5,958,047	09/28/99	Panwar	712	237		Jun. 25, 1997
			Duplicate	See paper #6 Filed 11/16/2000			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	WO 96/24895	08/15/96	PCT/US	G06F	9/22		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)

		Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)
		Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)
		Anton Chernoff, et al., FX!32, A Profile-Directed Binary Translator, IEEE Micro, Vol. 18 No. 2 pp. 56-64, March/April, 1998
		Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm
		Ebciooglu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996)
		Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)

EXAMINER

DATE CONSIDERED

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

ATTY. DOCKET NO.

30585/3

SERIAL NO.

09/385,394

APPLICANT

John S. Yates, Jr. et al.

FILING DATE

August 30, 1999

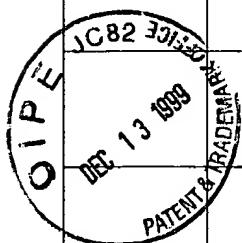
GROUP/ART UNIT

2751

RECEIVED

DEC 15 1999

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)



		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64: Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)
		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64: Technology and Market Forecast, Executive Summary, Microprocessor Report, MicroDesign Resources (July 20, 1998)
		Linley Gwennap, First Merced Patent Surfaces, Intel Document Reveals Processors With Dual Instruction Sets, Microprocessor Report, MicroDesign Resources (July 20, 1998)
		Raymond J. Hookway, et al., Combining Emulation and Binary Translation (August 28, 1997) http://www.digital.com/info/DTJP01HM.HTM
		Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)
		Nanodata Corporation, QM-1 Hardware level User's Manual (1975)
		Nanodata Corporation, QM-2 Processor (1976)
		Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)
		Robert F. Rosin, An Environment for Research in Microprogramming and Emulation, Communications of the ACM, Vol. 15 No. 8, pp. 748-760 (August 1972)
		Standard Computer Corp., IC-9000 Central Processing Unit - Principles of Operation (undated - possibly 1969)
		W.T. Wilner, Design of the Burroughs B1700, Fall Joint Computer Conference, pp. 489-497 (1972)
		W.T. Wilner, Burroughs B1700 Memory Utilization, Fall Joint Computer Conference, pp. 579-586 (1972)
		Duplicate #6 See paper 11/16/2000 Filed

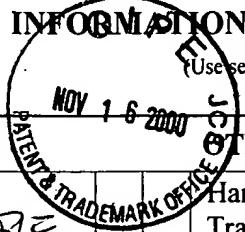
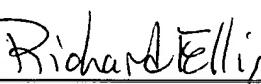
EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)			ATTY. DOCKET NO. 30585/3	SERIAL NO. 09/385,394		
			APPLICANT John S. Yates, Jr., et al.			
			FILING DATE August 30, 1999	GROUP ARTS UNIT 2751		
			RECEIVED Technology Center 2100 NOV 20 2000			
U.S. PATENT DOCUMENTS						
EXAMINER INITIALS <i>RLE</i>	TRADEMARK DOCUMENT NUMBER 3,544,969	DATE 12/01/1970	NAME Rakoczi	CLASS 340	SUBCLASS 172.5	SEARCHING/PAGE APPROPRIATE Nov. 27, 1967
<i>RLE</i>	3,781,823	12/25/1973	Senese	340	172.5	July 28, 1972
<i>RLE</i>	4,084,235	04/11/1978	Hirtle	364	200	Apr. 14, 1975
<i>RLE</i>	4,455,602	06/19/1984	Baxter	364	200	May 22, 981
<i>RLE</i>	4,514,803	4/30/1985	Agnew	364	200	Apr. 26 1982
<i>RLE</i>	4,575,797	03/11/1986	Gruner	364	200	May 22, 1981
<i>RLE</i>	5,481,684	01/02/1996	Richter	395	375	Jul. 20 1994
<i>RLE</i>	5,542,059	07/30/1996	Blomgren	395	375	Dec. 21, 1994
<i>RLE</i>	5,542,109	07/30/1996	Blomgren	395	800	Aug. 31, 1994
<i>RLE</i>	5,574,927	11/12/1996	Scantlin	395	800	Nov. 12, 1996
<i>RLE</i>	5,598,546	01/28/1997	Blomgren	395	385	Aug. 31, 1994
<i>RLE</i>	5,638,525	06/10/1997	Hammond	395	385	Feb. 10, 1995
<i>RLE</i>	5,652,869	07/29/1997	Herdeg	395	500	Dec. 28, 1994
<i>RLE</i>	5,685,009	11/04/1997	Blomgren	395	800	Nov. 29, 1995
<i>RLE</i>	5,774,686	06/30/1998	Hammond	395	385	Jun. 7, 1995
<i>RLE</i>	5,781,750	07/14/1998	Blomgren	395	500	Jan 11, 1994
<i>RLE</i>	5,822,578	10/13/1998	Frank	395	591	June 5, 1995
<i>RLE</i>	5,832,205	11/03/1998	Kelly	395	185.06	Aug. 20, 1996
<i>RLE</i>	5,842,017	11/24/1998	Hookway	395	707	Jan. 29, 1996
<i>RLE</i>	5,903,760	05/11/1999	Farber	395	707	Jun. 27, 1996
<i>RLE</i>	5,920,721	07G/06/1999	Hunter	395	705	Jun. 11, 1997
<i>RLE</i>	5,930,509	07/27/1999	Yates	395	707	Jan. 29, 1996
<i>RLE</i>	5,958,047	09/28/1999	Panwar	712	237	Jun. 25, 1997
<i>RLE</i>	6,000,028	12/07/1999	Chernoff	712	226	Jan. 29, 1996
<i>RLE</i>	6,031,992	02/29/2000	Cmelik	395	705	Jul. 5, 1996
<i>RLE</i>	6,091,897	07/18/2000	Yates	395	708	Jan. 29, 1996
FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER EP 0 324 308 <i>Larsen</i>	DATE 07/19/1989	COUNTRY EPO	CLASS G06F	SUBCLASS 9/30	TRANSLATION YES
<i>RLE</i>	WO 96/24895 <i>Hammond</i>	08/15/1996	PCT/US <i>WO</i>	G06F	9/22	NO
<i>RLE</i>	WO 97/50031 <i>Farber</i>	12/31/1997	PCT/US <i>WO</i>	G06F	9/455	
EXAMINER <i>Richard Ellis</i>	DATE CONSIDERED 1/31/2002					

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 30585/3	SERIAL NO. 09/385,394
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				APPLICANT John S. Yates, Jr., et al.	
		FILING DATE August 30, 1999	GROUP ART. UNIT 2751		NOV 20 2000 RECEIVED Technology Center 2100 TRANSLATION YES NO
FOREIGN PATENT DOCUMENTS					
RUE		DOCUMENT NUMBER WO 98/13740 Wing	DATE 04/02/1998	COUNTRY WIPO	CLASS G06F
RUE		WO 98/28689 Wing	07/02/1998	WIPO	G06F 11/00
RUE		WO 98/38575 Klaiber	09/03/1998	WIPO	G06F 11/00
RUE		WO 98/59292 Emelik	12/30/1998	WIPO	G06F 9/45
RUE		WO 99/03037 Kelly	01/21/1999	WIPO	G06F 11/00
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)					
RUE		Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)			
RUE		Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)			
RUE		Anton Chernoff, et al., FX!32, A Profile-Directed Binary Translator, IEEE Micro, Vol. 18 No. 2 pp. 56-64 (March/April, 1998)			
RUE		Compaq Computer Corp., Compiler Writer's Guide for the Alpha 21264, 1999			
RUE		Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm			
RUE		Ebciooglu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996)			
RUE		Linda Geppert et al., Transmeta's Magic Show, IEEE Spectrum, vol. 37 no. 5, pp. 26-33 (May 2000)			
RUE		Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)			
RUE		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64 Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)			
RUE		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64: Technology and Market Forecast, Executive Summary, Microprocessor Report, MicroDesign Resources (July 20, 1998)			
RUE		Linley Gwennap, First Merced Patent Surfaces, Intel Document Reveals Processors With Dual Instruction Sets, Microprocessor Report, MicroDesign Resources (July 20, 1998)			
RUE		Raymond J. Hookway, et al., Combining Emulation and Binary Translation (August 28, 1997) http://www.digital.com/info/DTJP01HM.HTM			
RUE		Intel Corporation, Intel Architecture Software Developer's Manual, <i>passim</i> (1997)			
EXAMINER Richard Ellis		DATE CONSIDERED 1/31/2002			

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 30585/3	SERIAL NO. 09/385,394
INFORMATION DISCLOSURE CITATION <small>(Use several sheets if necessary)</small>		APPLICANT John S. Yates, Jr., et al.			
		FILING DATE August 30, 1999	GROUP ART UNIT 2751		
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)					
 NOV 16 2000 RLE Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)					
RLE Nanodata Corporation, QM-1 Hardware level User's Manual (1975)					
RLE Nanodata Corporation, QM-2 Processor (1976)					
RLE Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)					
RLE Robert F. Rosin, An Environment for Research in Microprogramming and Emulation, Communications of the ACM, Vol. 15 No. 8, pp. 748-760 (August 1972)					
RLE Standard Computer Corp., IC-9000 Central Processing Unit - Principles of Operation (undated - possibly 1969)					
RLE W.T. Wilner, Design of the Burroughs B1700, Fall Joint Computer Conference, pp. 489-497 (1972)					
RLE W.T. Wilner, Burroughs B1700 Memory Utilization, Fall Joint Computer Conference, pp. 579-586 (1972)					
EXAMINER 		DATE CONSIDERED 11/31/2002			